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High-level synthesis (HLS), sometimes referred to as C synthesis, electronic system-level (ESL) synthesis, algorithmic synthesis, or behavioral synthesis, is an automated design process that interprets an algorithmic description of a desired behavior and creates digital hardware that implements that behavior.

## **High-level synthesis - Wikipedia**

1 EE 382V: SoC Design, Fall 2009 J. A. Abraham HLS 1 High Level Synthesis • Data Flow Graphs • FSM with Data Path • Allocation • Scheduling • Implementation • Directions in Architectural Synthesis

## **High Level Synthesis - University of Texas at Austin**

High-Level Synthesis: from Algorithm to Digital Circuit should be on each designer's and CAD developer's shelf, as well as on those of project managers who will soon embrace high level design and synthesis for all aspects of digital system design.

## **High-Level Synthesis | SpringerLink - link.springer.com**

High-level synthesis systems start with an abstract behavioral specification of a digital system and find a register-transfer level structure that realizes the given behavior.

## **(PDF) High-level synthesis of digital systems - ResearchGate**

However, for many high-level programming languages, such as C/C++, the description of bitwise access and computation is not as direct as hardware description languages, and high-level synthesis of ...

## **(PDF) Bit-level optimization for high-level synthesis and**

High-level synthesis raises the design abstraction level and allows rapid generation of optimized RTL hardware for performance, area, and power requirements. This article gives an overview of state-of-the-art HLS techniques and tools. Tim Cheng, Editor in Chief 8 0740-7475/09/\$26.00

## **An Introduction to High-Level Synthesis**

Designing at higher levels of abstraction is an obvious way as it allows a better coping with the system design complexity, to verify earlier in the design process and to increase code reuse. This book presents an excellent collection of contributions addressing different aspects of high-level synthesis from both industry and academia.

## **High-Level Synthesis - from Algorithm to Digital Circuit**

Next, high-level synthesis is used to generate a macro- scopic structure of the biochip; this structure is analogous to a structural RTL model in electronic CAD.

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